Notice of References Cited Application/Control No. 10/771,023 Examiner Eric Wendler Applicant(s)/Patent Under Reexamination LUNG, HSIANG LAN Page 1 of 1

U.S. PATENT DOCUMENTS

*		. Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-5,343,437	08-1994	Johnson et al.	365/230.03
	В	US-5,511,020 A	04-1996	Hu et al.	365/185.28
	C	US-5,586,073 A	12-1996	Hiura et al.	365/185.01
	D	US-5,691,552 A	11-1997	Oyama, Kenichi	257/316
	E	US-5,774,400 A	06-1998	Lancaster et al.	365/185.3
	F	US-5,953,254 A	09-1999	Pourkeramati, Ali	365/185.26
	G	US-6,307,807	10-2001	Sakui et al.	365/238.5
	Н	US-2001/0048612 A1	12-2001	Yi et al.	365/185.17
	.1	US-6,894,924 B2	05-2005	Choi et al.	365/185.01
	J	US-2004/0130942	07-2004	Yeh et al.	365/185.01
	к	US-			
	L	US-			
	м	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	α				,	
	R					
	Ø					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	υ	Liao et al., "Process Techniques and Electrical Characterization for High-k (HfOxNy) Gate Dielectric in MOS Devices", Proceedings, 7th International Conference on Solid-State and Integrated Circuits Technology, Volume 1, Oct. 2004, 372-377				
	٧					
·	w					
	х					

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.